FIG. 1 (Prior Art)

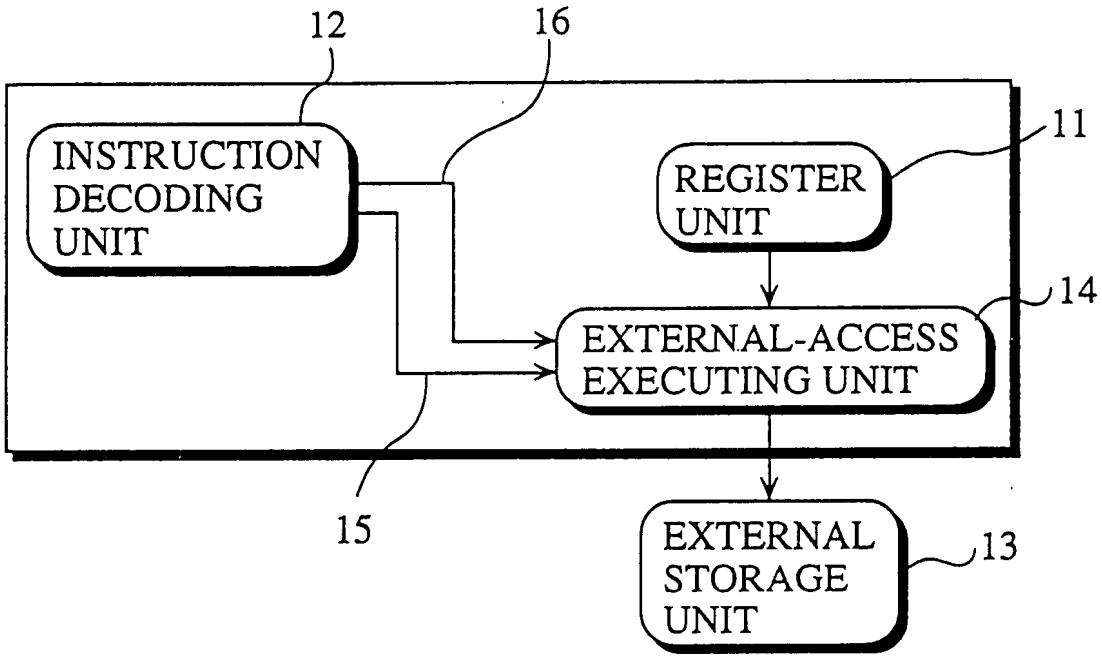

FIG. 2 (Prior Art)

OP	SIZE	SRC	DEST
MOVE	32bit	A1	@A2

FIG. 3 (Prior Art)

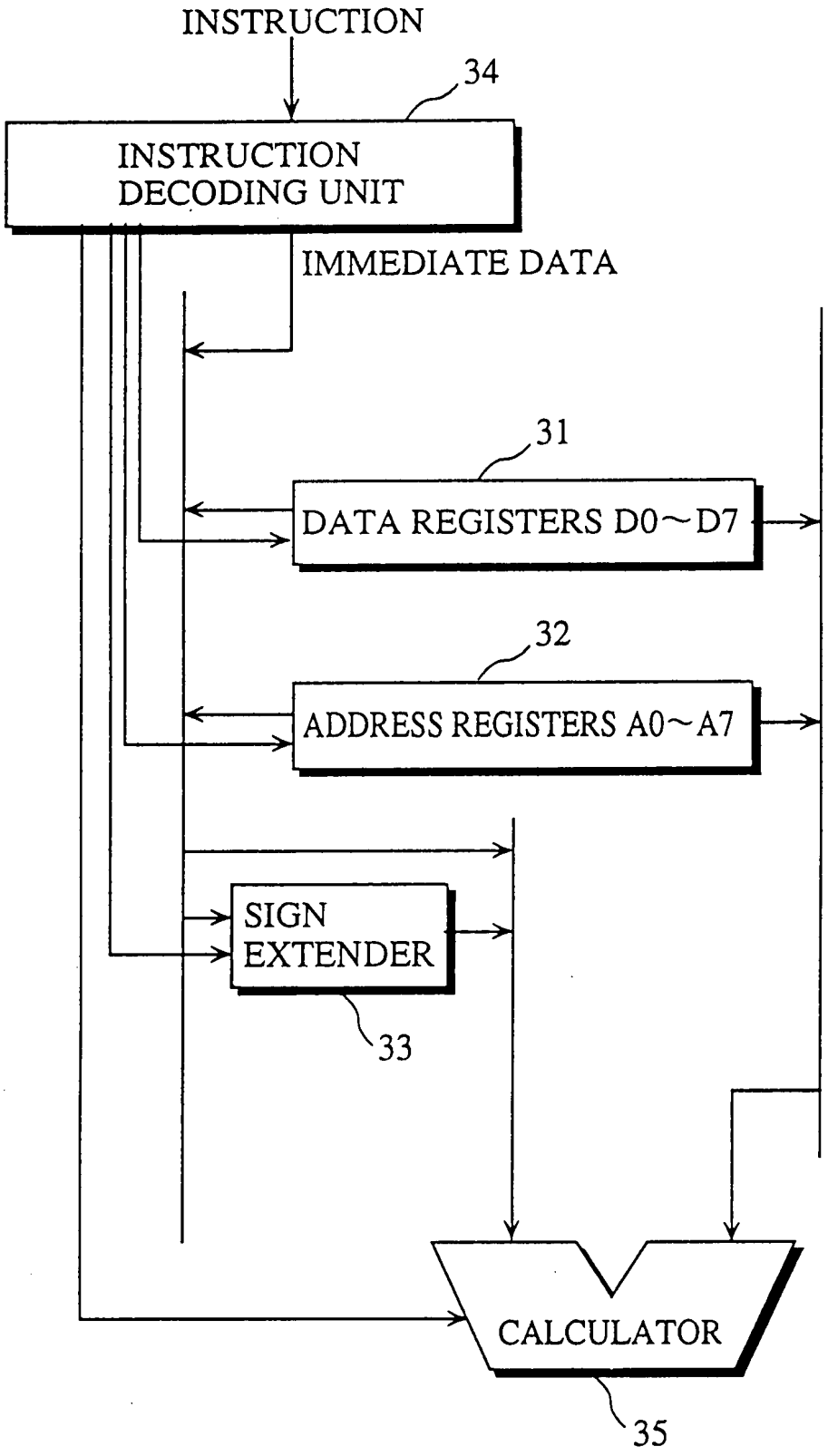

FIG. 4A (Prior Art)

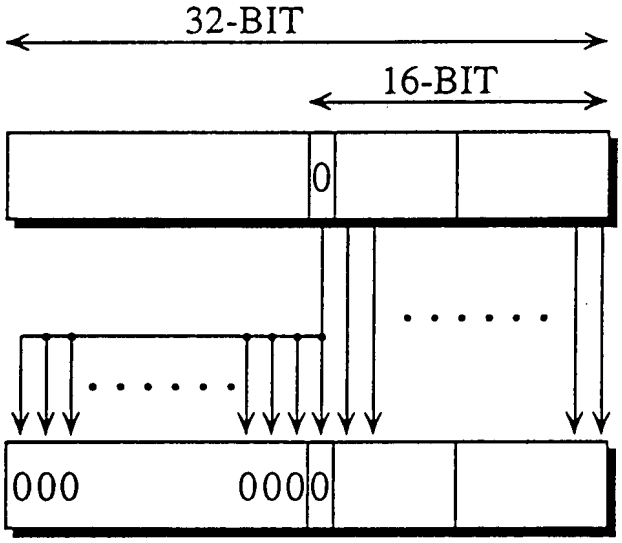

FIG. 4B (Prior Art)

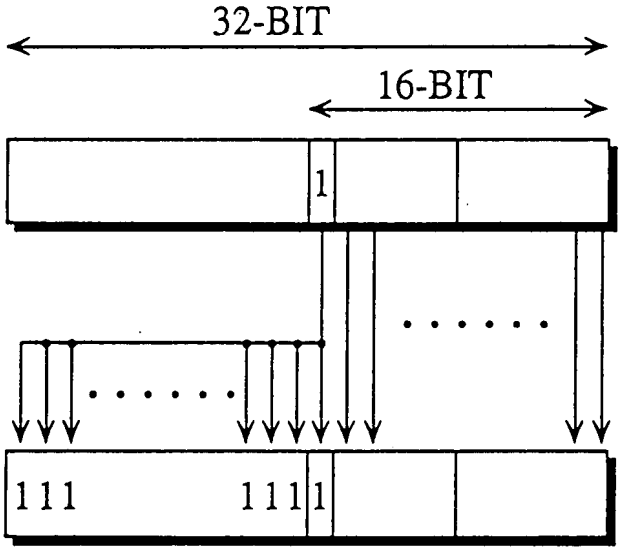

FIG. 5 (Prior Art)

```
            SUB   D0,D0            ——(1)
            MOVI  #H8000,A0        ——(2)→MOVI #H00008000,A0 (2')
LABEL A     MOV
            ADD   @A0,D1           ——(3)
            ADDI  D1,D0            ——(4)
            CMPI  #H0010,A0        ——(5)→ADDI #H00000010,A0 (5')
            BLT   #H8100,A0        ——(6)→CMPI #H00008100,A0 (6')
                  LABEL A          ——(7)
LABEL B     MOV
                  D0,@H10000000    ——(8)
```

FIG. 6 (Prior Art)

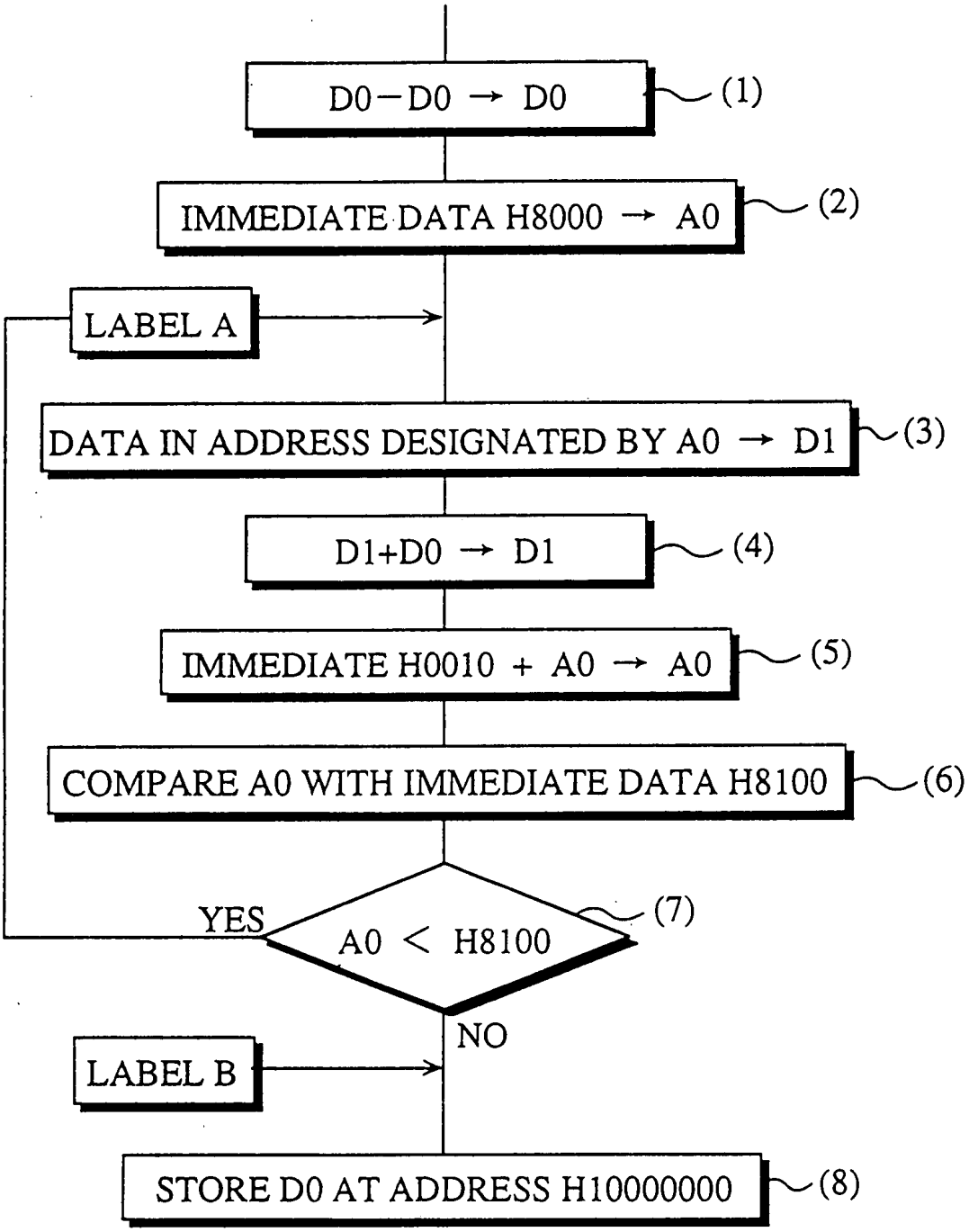

FIG. 7 (Prior Art)

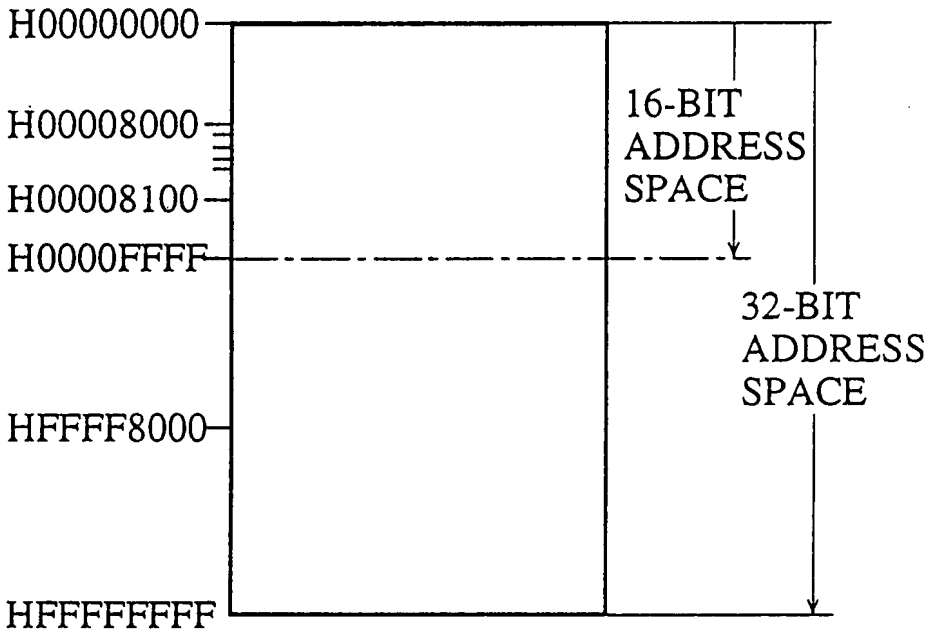

FIG. 8 (Prior Art)

```
            SUB   D0,D0              ——(1)
            MOVI  #H8000,A0          ——(2-1)
            ANDI  #H0000FFFF,A0      ——(2-2)
LABEL A
            MOV   @A0,D1             ——(3)
            ADD   D1,D0              ——(4)
            ADDI  #H0010,A0          ——(5)
            MOVI  #H8100,A0          ——(6-1)
            ANDI  #H0000FFFF,A1      ——(6-2)
            CMP   A1,A0              ——(6-3)
            BLT   LABEL A            ——(7)
LABEL B
            MOV   D0,@H10000000      ——(8)
```

FIG. 9 (Prior Art)

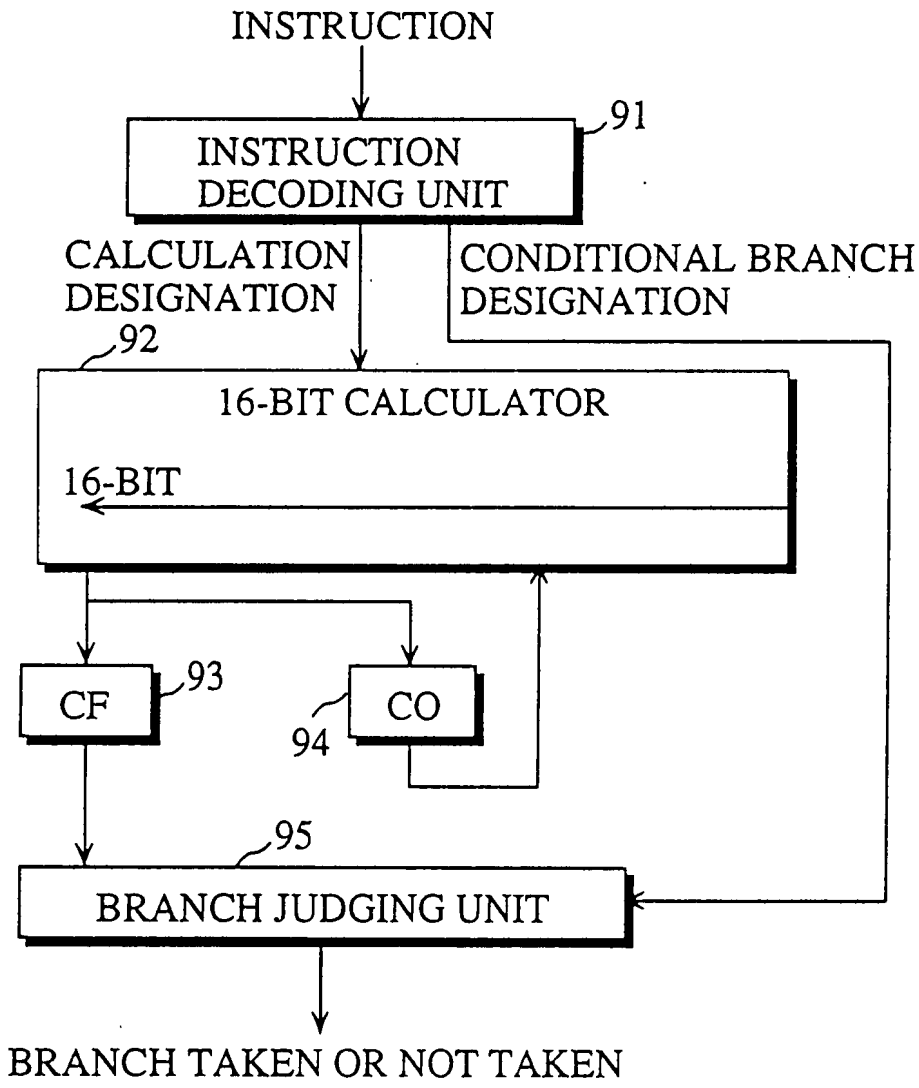

FIG. 10 (Prior Art)

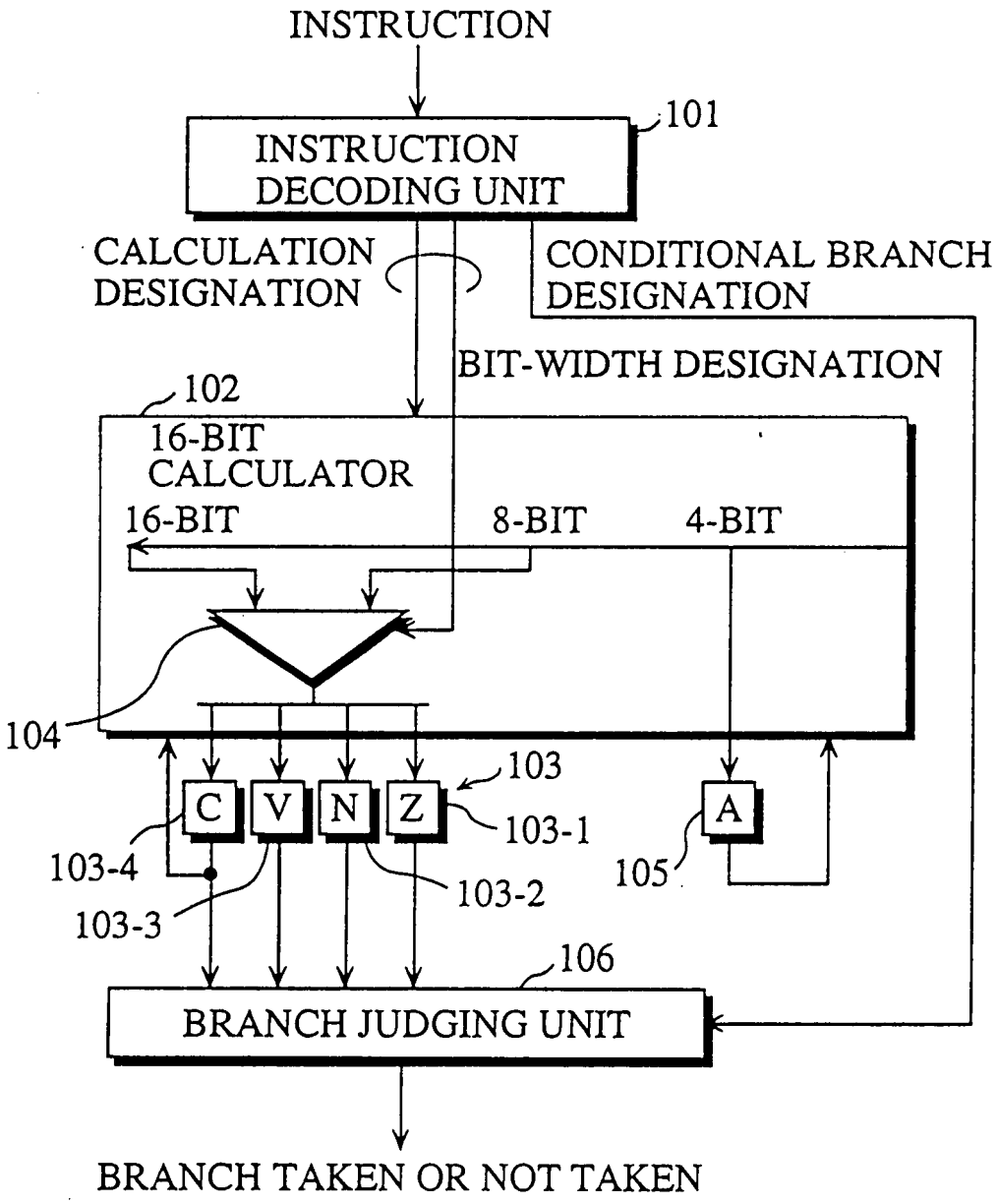

FIG. 11A (Prior Art)

ADD/SUBTRACT/COMPARE INSTRUCTIONS

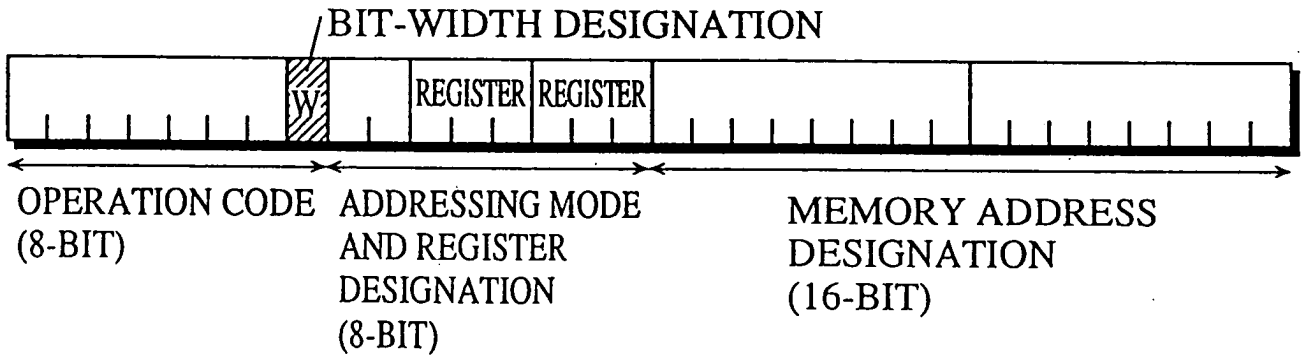

FIG. 11B (Prior Art)

CONDITIONAL BRANCH INSTRUCTION

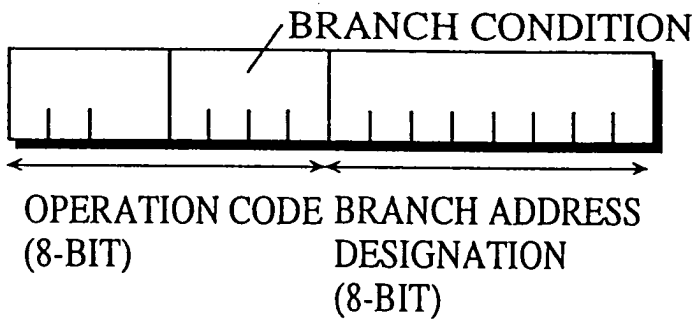

FIG. 12A (Prior Art)

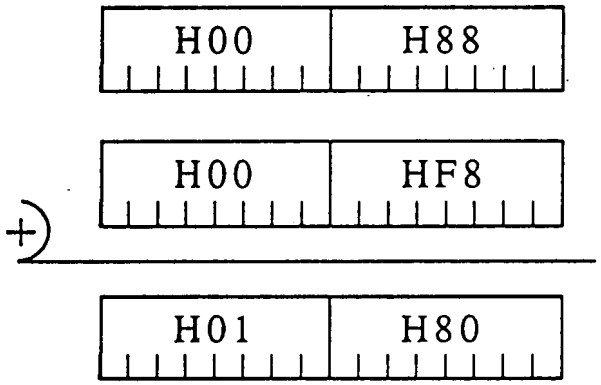
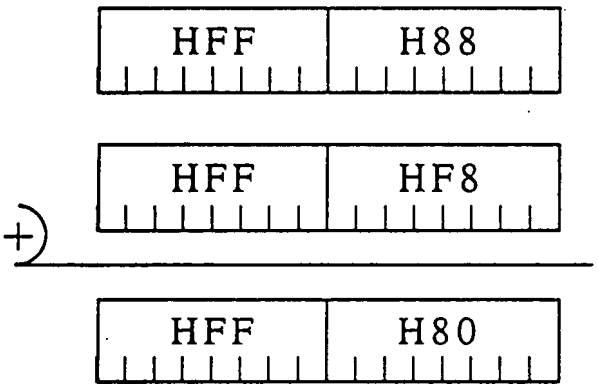
FIG. 12B (Prior Art)